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**TO: EXAMINER: Michelle Estrada**  
**EXAMINER'S TELEPHONE NUMBER: (703) 308-0792**  
**ART UNIT: 2823**  
**SERIAL NO.: 09/784,424**

**FROM: Aaron Waxler**  
**REGISTRATION NUMBER: 48,027**

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**JAN 6 - 2003**

**TECHNOLOGY CENTER 2800**

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I certify that these documents consisting of 5 pages (including this cover sheet and an amendment) is being transmitted via facsimile to the United States Patent and Trademark Office at the telephone number set forth above on 6 January, 2003.

  
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Youri Ponomarev

Atty. Docket: NL 000139

Serial No.: 09/784,424

Art Unit: 2823

Filed: February 15, 2001

Examiner: Michelle Estrada

Title: Semiconductor Device With an Integrated CMOS Circuit With  
MOS Transistors Having Silicon-Germanium ( $\text{Si}_{1-x}\text{Ge}_x$ ) Gate  
Electrodes, and Method of Manufacturing Same

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AMENDMENT

JAN 6 - 2003

Assistant Commissioner for Patents  
Washington, D.C. 20231

TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action dated October 7, 2002,  
Applicant submits the following amendments and remarks.

REMARKS

This application has been carefully reviewed in light of the  
Office Action dated October 7, 2002. Claims 1-4 remain pending in  
this application. Claim 1 is the independent claim. Favorable  
reconsideration is respectfully requested.

On the merits, the Office Action rejected Claim 2 under 35  
U.S.C. § 112 due to a missing basis of the percentage. The basis,  
"at%", is known in the art and is a measure of concentration. In  
view of this, Claim 2 is respectfully submitted for  
reconsideration.